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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	09/473,575
				Filing Date	December 28, 1999
				First Named Inventor	Darrell D. Boggs
				Group Art Unit	2755-2127
				Examiner Name	Unknown
Sheet	3	of	3	Attorney Docket Number	042390.P6871

[illegible][illegible]

Examiner Signature	<i>W. L. L. L.</i>	Date Considered	9/3/03
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Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication

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U.S. PATENT DOCUMENTS

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Application Number	09/473,575
Filing Date	December 28, 1999
First Named Inventor	Darrell D. Boggs
Group Art Unit	2755 2121
Examiner Name	Unknown ✓0
Attorney Docket Number	42390P6871

Sheet	4	of	4
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OTHER ART (Including Title, Author, Date, Pertinent Pages, etc.)

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**Examiner
Signature**

Thomas

Date	
Considered	

9/3/03

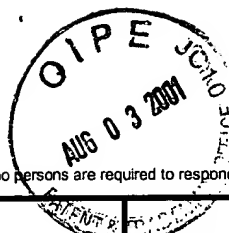
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OTHER ART (Including Title, Author, Date, Pertinent Pages, etc.)

Exam. Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
LU		R. GURU PRASADH, ET AL., A Benchmark Evaluation of a Multi-Threaded RISC Processor Architecture, 1991, pp 184-191, National Science Foundation and Cray Research Incorporated
		ROBERT A. IANNUCCI, ET AL., Multithreaded Computer Architecture: A Summary of the State of the Art, pp 167-200, Kluwer Academic Publishers, Boston/London/Dordrecht
		Proceedings, The 23rd Annual International Symposium on Computer Architecture, May 22-24, 1996, pp 191-202, ACM SIGARCH, IEEE Computer Society, TCCA, Philadelphia, Pennsylvania
		Proceedings, The 23rd Annual International Symposium on Computer Architecture, May 22-24, 1996, pp 203-212, ACM SIGARCH, IEEE Computer Society, TCCA, Philadelphia, Pennsylvania
		DENNIS LEE, ET AL. & BRAD CALDER, ET AL., Instruction Cache Fetch Policies for Speculative Execution, pp 1-11, Department of Computer Science
		SIMON W. MOORE, Multithreaded Processor Design, 1996, pp 1-141, Kluwer Academic Publishers, Boston/Dordrecht/London
		PETER SONG, Multithreading Comes of Age, July 14, 1997, pp 13-18, Microdesign Resources
		RUEDIGER R. ASCHE, Multithreading for Rookies, July 31, 1998, pp 1-15, Microsoft Developer Network Technology Group
✓		IEEE COMPUTER SOCIETY TECHNICAL COMMITTEE ON COMPUTER ARCHITECTURE, Second International Symposium on High-Performance Computer Architecture, February 3-7, 1996, pp291-301

Examiner Signature	<i>Thamir</i>	Date Considered	9/3/03
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